Instructor Information and Office hours

Michel Sayde, office and availability will be posted in cuLearn.

TAs Information and Office hours

TAs will be available in the labs. A schedule will be posted in cuLearn.

Calendar Information

Course Number: SYSC 3006
Course Title: Computer Organization
http://calendar.carleton.ca/undergrad/courses/SYSC/

Prerequisites

SYSC 2006 and ELEC 2607. This course precludes additional credit for SYSC 2001, SYSC 2003, SYSC 2320 and SYSC 3310. May not be taken for credit by students in Computer Systems Engineering, Communications Engineering, or Software Engineering.

Going into the course students are expected to understand and have knowledge of:

- logic gates, switching circuits and their logical behavior;
- Boolean algebra, truth tables of binary values and Karnaugh maps;
- basic knowledge of combinational circuits;
- basic knowledge of sequential circuits and state machine;
- binary arithmetic operations e.g. addition/subtraction and 2’s complement notation.

Students who have not satisfied the prerequisites for this course must either withdraw from the course or obtain a prerequisite waiver by visiting the Engineering Undergraduate Academic Support Office.
Course Objectives

Engineers working with microprocessors and microcontrollers (including mobile devices) must understand computer systems at this level, and these concepts form the foundation on which more powerful computer systems are based (such as desktop systems, servers, multiprocessors, and supercomputers). Therefore, this course introduces students to basic computer organization and functional design down to the gate-level abstraction, and to hardware/software interfacing.

Learning Outcomes

Upon completion of this course, students should be able to:

1) understanding how a computer works at hardware/software interface through the Instruction Set Architecture (ISA);
2) understanding microarchitecture (computer organization) to fetch an instruction from the memory and execute using the datapath;
3) understand hardware circuits necessary to interface input/output modules using polling and interrupt-based techniques;
4) design finite state machines necessary for the implementation of various instructions;
5) design hardware circuits at the core of microprocessors and microcontrollers;
6) develop machine code and corresponding assembly language programs to implement specific programming tasks;

Graduate Attributes (GA’s)

The Canadian Engineering Accreditation Board requires graduates of engineering programs to possess 12 attributes at the time of graduation. Activities related to the learning outcomes listed above are measured throughout the course and are part of the department’s continual improvement process. Graduate attribute measurements will not be taken into consideration in determining a student’s grade in the course. For more information, please visit: https://engineerscanada.ca/.

<table>
<thead>
<tr>
<th>Graduate Attributes</th>
<th>Learning Outcomes</th>
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<tbody>
<tr>
<td>1.10.E</td>
<td>Knowledge base: Discipline-specific concept DOE-7: <em>Digital systems and computers</em></td>
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<tr>
<td>1.5.S</td>
<td>Knowledge Base: Discipline-Specific Concept SCE-2: <em>Software Engineering</em></td>
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<tr>
<td>4.2</td>
<td>Design: Detailed design specifications and requirements</td>
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<td>4.5</td>
<td>Design: Design implementation / task(s) definition</td>
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<tr>
<td>5.2</td>
<td>Use of engineering tools: Document-processing and graphics packages</td>
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Textbooks (or other resources) if applicable

A specific textbook is not recommended in the course. Course notes and supporting material will be supplied via the course webpage (cuLearn) for student personal use.

All posted materials on this course webpage are intended for student registered in this course personal use only and may not be reproduced or redistributed without prior written consent of the author(s).

Evaluation and Grading Scheme

1. Labs: 25%
2. Mid-term test: 25%
3. Final exam: 50%

Breakdown of course requirements

- Laboratory: There will be 6 graded laboratories that will be posted on the course website. Lab attendance is mandatory, with work to be demonstrated by the end of the lab period. Marks will only be awarded for labs demonstrated during the student’s schedule lab time (according to section registration). During the scheduled lab times, TA assistance will be available. The computer lab is open seven days a week, whenever the building is open. You may use the lab at any time other than those timeslots when the lab is reserved for other courses or for other sections of this course. Students are encouraged to discuss issues when working on labs; however, they are expected to do their own lab work individually. Suspected plagiarism will be investigated and may result in a mark of zero for the lab. As well, alleged instructional offences will be reported to the Associate Dean of Engineering. (Please see the current undergraduate calendar, “Instructional Offences”, in the Undergraduate Calendar Supplement).

Students are warned that the labs and study questions form a very important part of this course – doing these (by yourself) is an excellent way for you to learn the material. In this context, it should be noted that copying labs is, even if you are not caught, a self-defeating exercise.

Historically, most of the students who resorted to copying did not do particularly well on the midterm or final exam.

- Midterm Exam: There will be one closed book, no-calculator, midterm exam. Midterm attendance is mandatory. Room arrangements will be announced during class and in the course website. It may be held outside of regular class time if necessary.
- **Final Exam**: A closed book, no-calculator, final exam will be held during the University's formal examination period. The final examination is for evaluation purposes only and will not be returned to students. You will be able to make arrangements with the instructor or with the department office to see your marked final examination after the final grades have been made available.

- **Study Questions**: Study questions may be posted on the course website as a complement to the lectures, but will not be collected and graded.

**Lecture Breakdown**: The following themes are interwoven in the lectures throughout the course. The number of lectures beside each theme indicates the (approximate) total amount of class time spent on that theme over the term.

- **Computer system architecture (basics) (6 lectures)**
  - Computer system components: processor, memory, I/O, interconnection bus
  - Information encoding, data representation in binary, hexadecimal
  - Number systems, unsigned integers, signed integers, 2’s complement, floating point
  - Computer hardware organization: datapath and control
  - Registers, instruction cycle

- **Hardware/software interface (Instruction Set Architecture) (4 lectures)**
  - Instructions: data manipulation, data transfer, control flow, instruction encoding
  - Computer arithmetic, flags

- **Microcontroller example (2 lecture)**
  - Microcontroller concept
  - System on Chip, memory model, ISA

- **Assembly language programming (6 lectures)**
  - Code snippets, examples
  - Assembly process, linker, loader

- **High-level Language Support (2 lecture)**
  - Variables, arrays, structures, assignment, looping, conditional statements
  - Procedures and functions, parameter passing

- **Peripheral I/O and Interrupts (2 lectures)**
  - Register model of peripheral devices: parallel I/O, serial I/O, timers
  - Polling
  - Hardware interrupts: vectored and prioritized, Nested Vector Interrupt Controller(NVIC)
  - Examples: timer, serial
  - Software interrupts, O/S calls
**General Regulations**

**Attendance:** Students are expected to attend all lectures and lab periods. The University requires students to have a conflict-free timetable. For more information, see the current *Undergraduate Calendar, Academic Regulations of the University, Section 2.1.3, Course Selection and Registration and Section 2.1.7, Deregistration*.

**Health and Safety:** Every student should have a copy of our Health and Safety Manual. A PDF copy of this manual is available online: [http://sce.carleton.ca/courses/health-and-safety.pdf](http://sce.carleton.ca/courses/health-and-safety.pdf)

**Deferred Term Work:** Students who claim illness, injury or other extraordinary circumstances beyond their control as a reason for missed term work are held responsible for immediately informing the instructor concerned and for making alternate arrangements with the instructor and in all cases this must occur no later than three (3.0) working days after the term work was due. The alternate arrangement must be made before the last day of classes in the term as published in the academic schedule. For more information, see the current *Undergraduate Calendar, Academic Regulations of the University, Section 4.4, Deferred Term Work*.

**Appeal of Grades:** The processes for dealing with questions or concerns regarding grades assigned during the term and final grades is described in the *Undergraduate Calendar, Academic Regulations of the University, Section 3.3.4, Informal Appeal of Grade and Section 3.3.5, Formal Appeal of Grade*.

**Academic Integrity:** Students should be aware of their obligations with regards to academic integrity. Please review the information about academic integrity at: [https://carleton.ca/registrar/academic-integrity/](https://carleton.ca/registrar/academic-integrity/). This site also contains a link to the complete Academic Integrity Policy that was approved by the University's Senate.

**Plagiarism:** Plagiarism (copying and handing in for credit someone else's work) is a serious instructional offense that will not be tolerated.

**Academic Accommodation:** You may need special arrangements to meet your academic obligations during the term. You can visit the Equity Services website to view the policies and to obtain more detailed information on academic accommodation at [http://www.carleton.ca/equity/](http://www.carleton.ca/equity/). For an accommodation request, the processes are as follows:

- **Pregnancy or Religious obligation:** Please contact your instructor with any requests for academic accommodation during the first two weeks of class, or as soon as possible after the need for accommodation is known to exist. For more details see [https://carleton.ca/equity/wp-content/uploads/Student-Guide-to-Academic-Accommodation.pdf](https://carleton.ca/equity/wp-content/uploads/Student-Guide-to-Academic-Accommodation.pdf)
- **Academic Accommodations for Students with Disabilities:** The Paul Menton Centre for Students with Disabilities (PMC) provides services to students with Learning Disabilities (LD), psychiatric/mental health disabilities, Attention Deficit Hyperactivity Disorder (ADHD), Autism Spectrum Disorders (ASD), chronic medical conditions, and impairments in mobility, hearing, and vision. If you have a disability requiring academic
accommodations in this course, please contact PMC at 613-520-6608 or pmc@carleton.ca for a formal evaluation. If you are already registered with the PMC, contact your PMC coordinator to send me your Letter of Accommodation at the beginning of the term, and no later than two weeks before the first in-class scheduled test or exam requiring accommodation (if applicable). Requests made within two weeks will be reviewed on a case-by-case basis. After requesting accommodation from PMC, meet with me to ensure accommodation arrangements are made. Please consult the PMC website (www.carleton.ca/pmc) for the deadline to request accommodations for the formally-scheduled exam (if applicable).

- **Survivors of Sexual Violence:** As a community, Carleton University is committed to maintaining a positive learning, working and living environment where sexual violence will not be tolerated, and where survivors are supported through academic accommodations as per Carleton’s Sexual Violence Policy. For more information about the services available at the university and to obtain information about sexual violence and/or support, visit: https://carleton.ca/sexual-violence-support/.

- **Accommodation for Student Activities:** Carleton University recognizes the substantial benefits, both to the individual student and for the university, that result from a student participating in activities beyond the classroom experience. Reasonable accommodation must be provided to students who compete or perform at the national or international level. Please contact your instructor with any requests for academic accommodation during the first two weeks of class, or as soon as possible after the need for accommodation is known to exist. For more details, see https://carleton.ca/senate/wp-content/uploads/Accommodation-for-Student-Activities-1.pdf

**Copyright on Course Materials:** The materials created for this course (including the course outline and any slides, posted notes, labs, project, assignments, quizzes, exams and solutions) are intended for personal use and may not be reproduced or redistributed or posted on any web site without prior written permission from the author(s).